

CLAIMS

1. A method of fabricating a semiconductor device by employing ion
implantation to provide a semiconductor substrate (1) at a surface thereof with a region
5 having dopant introduced therein, comprising the steps of: providing said semiconductor
substrate (1) at a surface thereof with a mask layer including a polyimide resin film (2);
and implanting dopant ions (5).

2. A method of fabricating a semiconductor device by employing ion
10 implantation to provide a semiconductor substrate (101) at a surface thereof with a
region having dopant introduced therein, comprising the steps of: providing said
semiconductor substrate (101) at a surface thereof with a mask layer (103) including a
SiO₂ film (107a, 107b) and a thin metal film (105); and implanting dopant ions (5).

15 3. The method of claim 1 or 2, wherein said semiconductor substrate (1, 101)
is a SiC semiconductor substrate.

4. The method of claim 1 or 2, wherein said mask layer is deposited on said
semiconductor substrate (1, 101) at a region to be undoped with dopant ions.

20 5. The method of claim 1 or 2, wherein said dopant ions are implanted into a
region unmasked by said mask layer.

25 6. The method of claim 1, wherein said semiconductor substrate (1) is heated
to at least 300°C and dopant ions (5) are implanted.

7. The method of claim 1, wherein said semiconductor substrate (1) is heated
to at least 500°C and dopant ions (5) are implanted.

8. The method of claim 1, wherein said polyimide resin film (2) is formed of photosensitive polyimide resin.

5 9. The method of claim 1, wherein said polyimide resin film (2a) has a thickness of at least twice a depth of dopant introduced into said semiconductor substrate (1) at a region free of said polyimide resin film (2a).

10 10. The method of claim 1, wherein a thin metal film is posed between said polyimide resin film (2a) and said semiconductor substrate (1).

11. The method of claim 1, wherein a thin film formed of SiO_2 is posed between said polyimide resin film (2a) and said semiconductor substrate (1).

15 12. The method of claim 2, wherein said semiconductor substrate (101) is heated to at least 300°C to 500°C and dopant ions are implanted.

13. The method of claim 2, wherein said semiconductor substrate (101) is heated to at least 500°C to 800°C and dopant ions are implanted.

20 14. The method of claim 2, wherein said mask layer (103) is formed of at least three layers.

25 15. The method of claim 2, wherein said SiO_2 film (107a, 107b) and said thin metal film (105) each have an average thickness of 500 nm to $1.5\ \mu\text{m}$.

16. The method of claim 2, wherein said mask layer (103) includes a SiO_2 film as a film corresponding to a bottommost layer.

17. The method of claim 2, wherein said mask layer (103) includes a thin metal film as a film corresponding to a bottommost layer.

5 18. The method of claim 2, wherein said mask layer (103) includes a SiO₂ film as a film corresponding to a topmost layer.

19. The method of claim 2, wherein said mask layer (103) includes a thin metal film as a film corresponding to a topmost layer.

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20. The method of claim 2, wherein said SiO₂ film (107a, 107b) is formed by SOG.